

ABSTRACT OF THE DISCLOSURE

Disclosed herein is a method and apparatus to improve the linearity of pipelined switched-capacitor digital to analog converters by balancing the settling behavior of its pre-charge switches. In more detail, a switched capacitor DAC includes a number of substantially identical cells, one cell for each bit of an input digital word. A number of switch driver circuits are used to apply respective switch control signals to turn respective switches on and off. Advantageously, the switch control signals differ by an amount determined to equalize the gate-to-source voltage difference between different switches.